

Please replace the following claims as rewritten below:

2. (Amended) A device as in claim 3 wherein the differential data bus comprises a differential twisted pair line.

3. (Amended) A device for changing a termination voltage of a differential data bus, the differential data bus comprising a first data bus and a second data bus, the device comprising:

a first adjustable termination path connectable to the first data bus;

a second adjustable termination path connectable to the second data bus; and

a switch connectable in parallel with the first adjustable termination path and the second adjustable termination path;

wherein the first adjustable termination path is 50 ohm to -2 volts or 100 ohms between the first data bus and the second data bus.

4. (Amended) A device for changing a termination voltage of a differential data bus, the differential data bus comprising a first data bus and a second data bus, the device comprising:

a first adjustable termination path connectable to the first data bus;

a second adjustable termination path connectable to the second data bus; and

a switch connectable in parallel with the first adjustable termination path and the second adjustable termination path;

wherein the second adjustable termination path is 50

ohm to -2 volts or 100 ohms between the first data bus and the second data bus.

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Cont
5. (Amended) A device as in claim 3 wherein the switch is a field effect transistor (FET).

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7. (Amended) A method as in claim 9 wherein the step of connecting the variable termination to the differential data bus further comprises the step of:

connecting a field effect transistor (FET) to the variable termination, wherein the FET enables the first termination path or the second termination path.

8. (Amended) A method as in claim 9 wherein the step of enabling the first termination path further comprises the step of configuring the first termination path to be 100 ohms between the first data bus and the second data bus.

9. (Amended) A method for changing terminations in an emitter coupled logic (ECL) transceiver having a differential data bus, the method comprising the steps of:

connecting a variable termination to the differential data bus, wherein the variable termination is a first termination path or two second termination paths, the differential data bus comprising a first data bus, and a second data bus;

enabling the first termination path when the ECL transceiver is in a receive mode; and

enabling the two second termination paths when the ECL transceiver is in a transmit mode;

wherein the step of enabling the two second termination paths further comprises the steps of:

configuring a first one of the two second termination paths to be 50 ohms between the first data bus and a -2vdc source; and

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Cob configuring a second one of the two second termination paths to be 50 ohms between the second data bus and the -2vdc source.

11. (Amended) An apparatus as in claim 12 wherein the variable termination further comprises :

93 a field effect transistor (FET), wherein the FET enables the first termination path or the second termination path.

12. (Amended) An apparatus for changing terminations in an emitter coupled logic (ECL) transceiver having a differential data bus, the apparatus comprising:

132 a variable termination connectable to the differential data bus, the variable termination comprising a first termination path or two second termination paths, the differential data bus having:

a first data bus connectable to the ECL transceiver; and

a second data bus connectable to the ECL transceiver;

wherein the first termination path further substantially comprises 50 ohms to -2 volts or 100 ohms between the first data bus and the second data bus.

13. (Amended) An apparatus for changing terminations in an emitter coupled logic (ECL) transceiver having a differential data bus, the apparatus comprising:

a variable termination connectable to the differential data bus, the variable termination comprising a first termination path or two second termination paths, the differential data bus having:

a first data bus connectable to the ECL transceiver; and

a second data bus connectable to the ECL transceiver;

wherein the two second termination paths further comprise:

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Cond a first one of the two second termination paths to be 50 ohms between the first data bus and a -2vdc source; and

a second one of the two second termination paths to be 50 ohms between the second data bus and the -2vdc source.

94 18. (Amended) At least one program storage device readable by a machine, tangibly embodying a program of instructions executable by the machine to perform method steps for changing terminations in a programmable logic device (PLD) having a multi-mode data bus, the method comprising the steps of:

connecting a variable termination to the multi-mode data bus, wherein the variable termination is a first termination path or two second termination paths, the multi-mode data bus having:

a first data bus connectable to the PLD; and

a second data bus connectable to the PLD;

wherein a resistive circuit connects the first and second data buses to each other and a switch